



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: DeNatale et al.

Serial No. 10/676,875

Filed: September 30, 2003

Title: 1:N MEM SWITCH MODULE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)**

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the documents listed on the attached Form PTO-1449. This Information Disclosure Statement is being filed within three months of the filing of the above referenced application or before the mailing date of a first Office Action on the merits for the above-referenced application.

In accordance with the PTO procedure, (all patent applications filed after June 30, 2003), we have not enclosed the U.S. patent references cited on PTO form 1449.

Applicants respectfully request that the Examiner consider the listed documents and evidence that consideration of relevant portions thereof by making appropriate notations on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that each or all of the listed documents are material or constitute "prior art". If the Examiner applies any of the documents as prior art against any claim in the application and applicants determine that the cited documents do not

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on

1/14/04  
Date

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constitute "prior art" under United States law, applicants reserve the right to present to the Office the relevant facts and law regarding the appropriate status of such documents.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed documents, should one or more of the documents be applied against the claims of the present application.

Respectfully submitted,



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Dated: 1-14-04

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## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent)		
	1	A SURFACE MICROMACHINED MINIATURE SWITCH FOR TELECOMMUNICATIONS APPLICATIONS WITH SIGNAL FREQUENCIES FROM DC UP TO 4 GHZ, Yao and Chang, In Tech. Digest, Transducer-95, Stockholm, Sweden, June 25-29, 1995, pp 384-387.
	2	A DC-TO40 GHZ FOUR-BIT FR MEMS TRUE-TIME DELAY NETWORK, Hacker et al., IEEE Microwave and Wireless Components Lett., Vol. 11, pp 56-58, Feb. 2001
	3	SOLID STATE CIRCUITS/CONTROL CIRCUITS, THE ELECTRICAL ENGINEERING HANDBOOK, 1993, p. 1011-1013.
	4	RF MEMS THEORY, DESIGN, AND TECHNOLOGY, Gabriel M. Rebeiz, 2003, 9 MEMS PHASE SHIFTERS, p. 259-295.
Examiner		Date Considered
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		
Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE		